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IEEE JNL IEEE Journal or Magazine

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IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

1. A Methodology for Automatic Hardware Synthesis of Multiplier-less Digital Filters with Preset Accuracy

Chan, S. C.; Tsui, K. M.; Zhao, S. H.;
Circuits and Systems, 2006, APCCAS 2006, IEEE Asia Pacific Conference on
4-7 Dec. 2006 Page(s):61 - 64
Digital Object Identifier 10.1109/APCCAS.2006.342296

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2. Synthesis and characterization of Cu-doped SrTiO₃/powders and sol-gel processed by MgO templates

Ayala, A.; Holesinger, T.G.; Clem, P.G.; Matias, V.; Jia, Q.X.; Haiyan Wang; Folty, S.R.; Gibbons, Applied Superconductivity, IEEE Transactions on
Volume 15, Issue 2, Part 3, June 2005 Page(s):2703 - 2706
Digital Object Identifier 10.1109/TASC.2005.847787

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3. Coefficient quantization error recovery

Suarez, J.I.; Lindquist, C.S.;
Circuits and Systems, 1999, ISCAS '99, Proceedings of the 1999 IEEE International Symposium on
Volume 3, 30 May-2 June 1999 Page(s):435 - 438 vol.3
Digital Object Identifier 10.1109/ISCAS.1999.778878

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4. Performance analysis by topology indexed lookup tables

Agarwal, P.; Vidyarthi, A.; Madden, P.H.;
Circuits and Systems, 2005, ISCAS 2005, IEEE International Symposium on
23-26 May 2005 Page(s):3579 - 3582 Vol. 4
Digital Object Identifier 10.1109/ISCAS.2005.1465403

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5. Minimization of adders in fast FIR digital filters and its application to filter design

Yagyu, M.; Nishihara, A.; Fujii, N.;
Circuits and Systems, 1996, ISCAS '96, 'Connecting the World', 1996 IEEE International Symposium on
Volume 2, 12-15 May 1996 Page(s):293 - 296 vol.2

Digital Object Identifier 10.1109/ISCAS.1996.541704

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